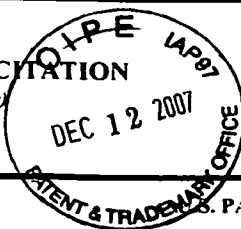


*(Use several sheets if necessary)*



MA-583-66

**10/642,481**

Enomoto, et al.

**August 18, 2003**

**Not Yet Assigned**

## 8. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/C.B./		<p><b>Youichi HIDAKA, et al., Institute of Electronics, Information and Communication Engineers, society Meeting 2002, B-7-11 to B-7-13," Proposal of Ethernet Architecture GOE (Global Optical Ethernet) for Future Generation", pp. 171-173.</b></p>

**EXAMINER** /Christopher Biagini/

**DATE CONSIDERED** 03/05/2008

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.